Priority Application Serial No	
Priority Filing Date	January 19, 2001
Inventor	Kunal R. Parekh et al.
Assignee	Micron Technology, Inc.
Priority Group Art Unit	2813
Priority Examiner	Y. Huynh
Attorney's Docket No	MI22-1816
Title: Capacitors, DRAM Arrays, Monolithic Int	egrated Circuits, And Methods
of Forming Capacitors, DRAM Arrays	s, And Monolithic Integrated
Circuits	

## PRELIMINARY AMENDMENT

To:

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

Frederick M. Fliegel, Ph.D.

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Sir:

This is a preliminary amendment accompanying a Request for Continuation Application for the above-entitled patent application. Prior to examining the application, please enter the following amendments.

## **AMENDMENTS**